# " S P A R K S "

# THE NORTH SHORE AMATEUR RADIO CLUB INC. MARCH 1981 EDITION

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Editors:-	Bernie Sandbrod Eric Atkins	ok VE3ATI VE3HMG	

Club Repeater: VE3OSH 147.72 In 147.12 Out

Club Station: VE3NSR

NETS:- 2 Meter Net meets every Thursday at 19:30, with Roy VE3AAF at control.

10 Meter Net meets Sundays at 10:00 on 28.200 Mhz. with george VE3GOU at control.

# NEXT MEETING

Tuesday, March 10th, 1981 in the cafeteria of O'Neill C.I.

TIME - 20:00

VE3HMG Eric will be giving a demonstration of the "Tone-Alert".

# TONE ALERT PROJECT

In the January project?". So, think its a use nuary issue '. So, after s a useful pr project. of "Spa arks for I b L P while ead "What a while the about e idea a clu club group ame and I

If you don't would like t putting in a iron Ø ţ like to listen to monitor in case phone call for a tο ham of an emergency or wouldn ham in need, warm up your 1 te soldering r but

The the tone / pe Alert" decodes ted. H er sent e are Ø by a temperature touch the tone one pa ad t-'n and Ω joodi: N.

- 4 1 'nа Dual d can tone **9**0 e decoding, turned off a t one e for en emergenci O ហ and one fo Ř low priority
- 9 50K c pad if a on decoders that national standard Ω can 0 0 0 0 rt tun ed to K H н quenc
- ω Led ហ g ct he P.C. board ç assi ល rt Ľ. bringing decod er ហ 92 requency
- 4. Whi Ch h Led ds for e emergency one and has low priority that latch been activated in your 9 absence. until н eset
- 5. has repeater. C Circuit for o (thanks opening d so you to Mike VE3FIV speaker when Pur won't double wi PTT bu with s button on someone suggestion). n the mid microphone ady on the

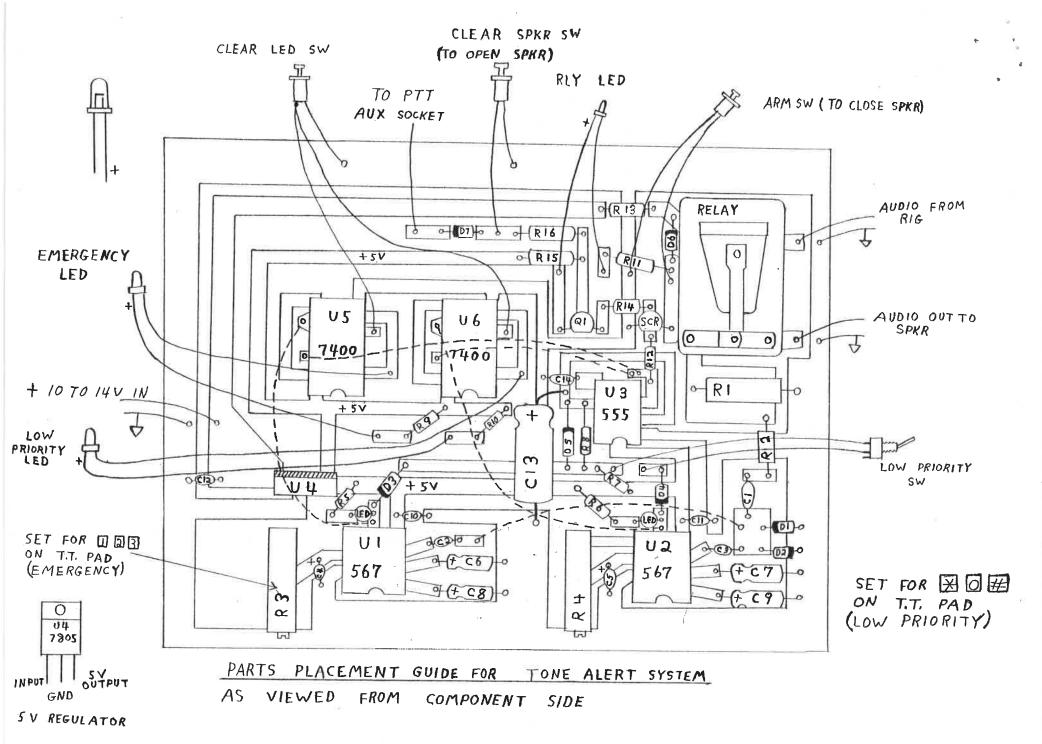
external speaker the auxillary soc "Tone Alert" ker plugs socket on g into into n the the rig. external T/A. The speaker 12V coul aker ja could ack, and taken from

careful for the Considering all the shopping board. the features, and about 15 the parts li bucks later ist isn't too bad you should have isn't bad. So Some parts

There is a layou large lines. It will need is a pammount of etcha few boards available your own and see how layout for the PC board in this s. It shouldn"t be hard to make is a piece of PC board, tracing etchant and a drill. If time pe available (for a price), but, and see how simple it is. this ng paper, marking pen, permits, there will be , I would rather you make your own board. All you bulletin. Notice a small

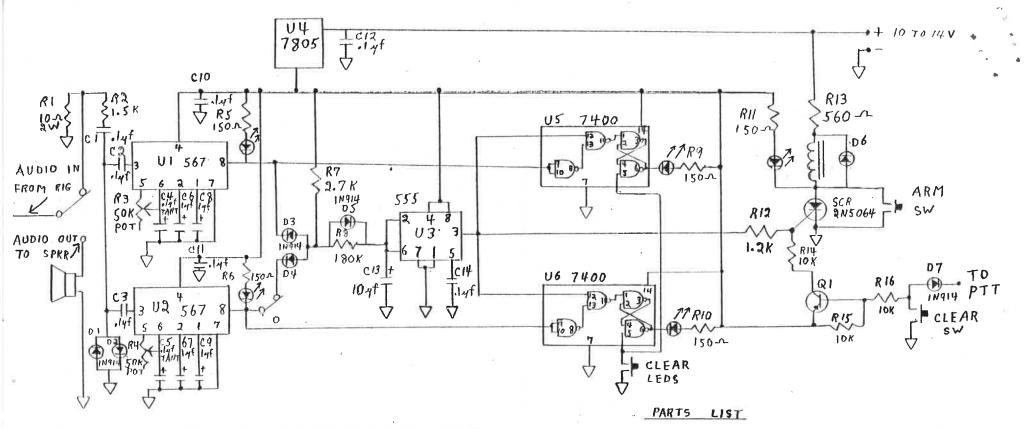
along wi Ø will with and your i all be D domo pad that ď o at and are the next meeting pencil and i will interested try g ing ţo your quest explain the ion circuit

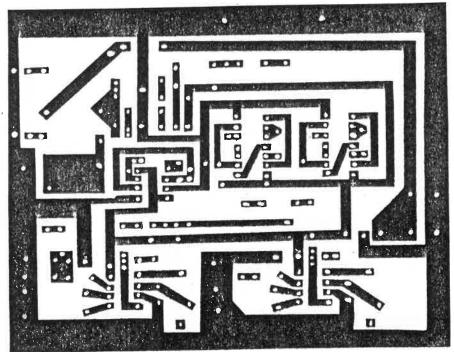
See you there,



BROKEN LINES \_\_\_\_ JUMPERS 5 /N ALL

FEB FRIDAY 13/81 GOOD LUCK
YE3HMG





COPPER SIDE OF PC BOARD

R1-10-2W
R2-1.5K4W
R3,4-15 TURN
POTS, BOURNS
3006P-1-503
R5,6,9,10,11-150-14W
R7-2.7K4W
R8-180K4W
R12-1.2K4W
R13 560-14W
R14,15,16-10K4W

C1, 2, 3, 10, 11, 12, 14
14 f 50V DISC CERAMIC

C4, 5-14 f 35V TANTALUM

C6, 7, 8, 9-14 f 63V ELECTROLYTIC

C13-104 f 50V ELECTROLYTIC

DI, 2, 3, 4, 5, 6, 7 - IN 914 OR IN HI48 DIODE
5 LEDS - TIL 220 OR ANY STANDARD TYPE
1 SCR 2N 5064
UI, U2 - LM 567CN - 8 TONE DECODER
U3 - LM 555 - 8 TIMER
U4 - 7805 TO - 220 PACKAGE V REG
U5, 6 - SN 7400N NAND GATE
Q1 PNP TRANSISTOR 2N 3905

3-8 PIN IC SOCKETS
2-14 """
1-RELAY RADIO SHACK 275-004
3-NORMALLY OPEN SWITCHES
RADIO SHACK 275-1547
1-SPST TOGGLE SWITCH

BOX FOR P.C. BOARD, SPKR-PLUGS, AUDIO CABLE, ETC.

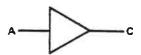
FEB 16/81 VE3HMG

# BASIC LOGIC SYMBOLS AND TRUTH TABLES

## BUFFER

OUTPUT

INPUT

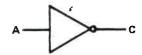


Α	С
0	0
1	1

### INVERTER

INPUT

OUTPUT



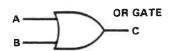
А	С
0	1
1	0

7-00	AND GATE
A ——	AND GATE
	)—-c
8-	

Α	В	С
0	0	0
1	0	0
0	1	0
1	1	1

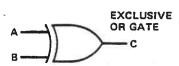


Α	В	С
0.	0	1
1	0	1
0	1	1
1	1	Ō



Α	В	С
0	0	0
1	0	1
0	1	1
1	1	1

Α	В	С
0	0	1
1	0	0
0	1	0
1	1	0



A	В	С
0	0	0
. 1	0	1
a_	11.	1
1	1	0

Α	B.	С
0	0	1
1	0	0
0	1	0
1.	1	1

# LAWS OF BOOLEAN ALGEBRA

A + 0 = A

A.A = A

A + 1 = 1

 $A + \overline{A} = 1$ 

A.0 = 0

 $A.\overline{A} = 0$ 

A + A = A

A.1 = A

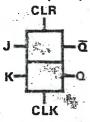
A.B + A.C = A(B + C)

A + B.C = (A + B)(A + C)

 $\overline{A.B.C} = \overline{A} + \overline{B} + \overline{C}$ 

 $\overline{A}.\overline{B}.\overline{C}. = \overline{A} + \overline{B} + \overline{C}$ 

# J.K. Flip Flep



INPUTS			OUTPUT	S	
CLR	CLK	J	K	Q	Q
.0	X	X	X	. 0	1
1	_0.	0	0	Qo	Qo
1	V	1	0	· 1	0
1.	T	0	1	0	1
1	J.	1.	1	TOG	GLE

Δ.

-HIGH LEVEL PULSE, DATA IS TRANSFERRED ON FALLING EDGE OF PULSE.

Qo

-THE LEVEL OF Q BEFORE INDICATED INPUT CONDITIONS WHERE ESTABLISHED.

TOGGLE -EACH OUTPUT CHANGES TO ITS COMPLEMENT ON EACH ACTIVE TRANSIENT (PULSE OF CLOCK).